

# DATA PROCESSING METHOD, SEMICONDUCTOR CIRCUIT, AND CERTIFICATION DEVICE

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**Classification:**

- international: *G06K19/07; G06F1/00; G06F21/22; G06K17/00; G06K19/073; G09C1/00; H04L9/10; H04L9/32; G06K19/07; G06F1/00; G06F21/22; G06K17/00; G06K19/073; G09C1/00; H04L9/10; H04L9/32; (IPC1-7): G06F1/00; G06K17/00; G06K19/07; G06K19/073; G09C1/00; H04L9/10; H04L9/32*

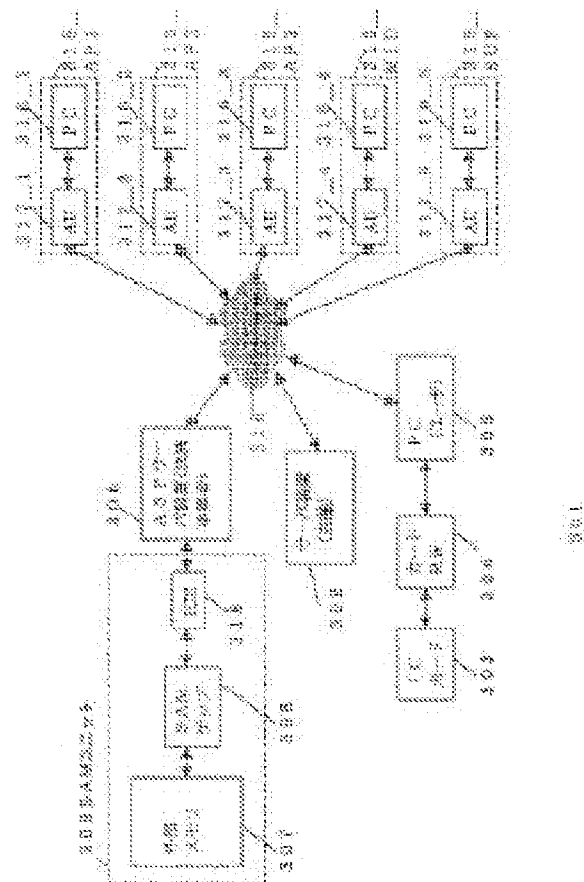
**- European:**

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Abstract of **JP 2002244756 (A)**

**PROBLEM TO BE SOLVED:** To provide a data processing method restricting access contents of a server device, etc., to a semiconductor circuit according to their rights. **SOLUTION:** A SAM (System Administration Manager) chip 308 has a software structure composed of a plurality of hierarchies and is capable of referring to download signature hierarchy key information according to respective hierarchies. When the SAM chip 308 receives a download demand from a personal computer 316-1, a verification unit 317-1 prepares download signature information, and the prepared download signature information is transmitted to the SAM chip 308. The SAM chip 308 verifies the received download signature information using the download signature hierarchy key information, and permits the download when it is determined to be proper.



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